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Agnes M. M. M.

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Ta Lee Yu

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(Use several sheets if necessary)

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OTHER DOCUMENTS (including Author, Title, Date, Portion(s) Pages, Etc.)

1. ✓	J. Chen et al., "Design and Layout of a High ESD Performance NPN Structure for Submicron BiCMOS Bipolar Circuits", IEEE Jnl 1996, 0-7803-2753-5/96, pp. 227-232.

ΔΙΛΥΝΕΙΤ

DATE COMPLETED

6/24/04

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